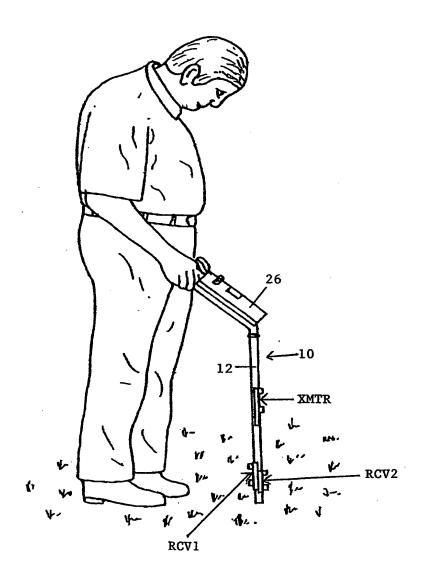
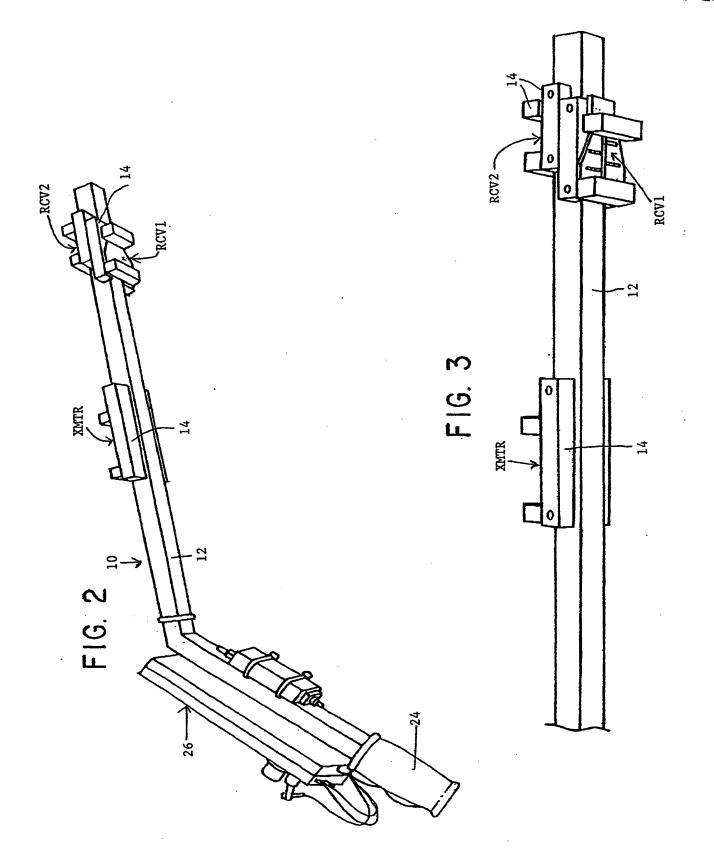
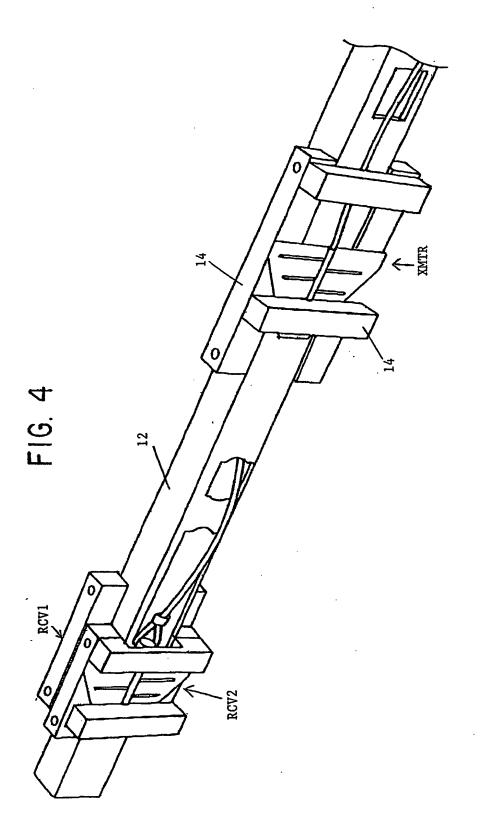
FIG. 1



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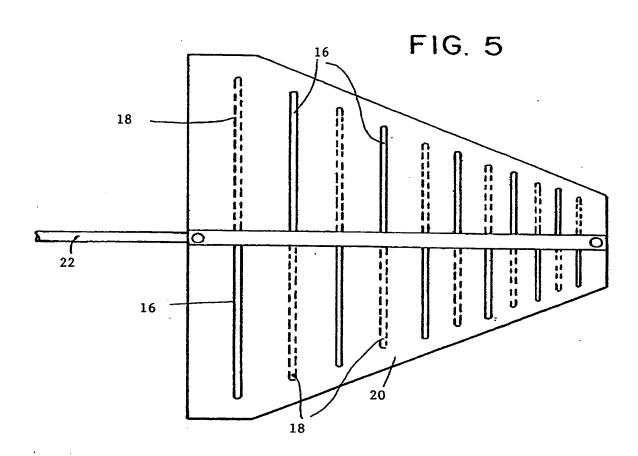
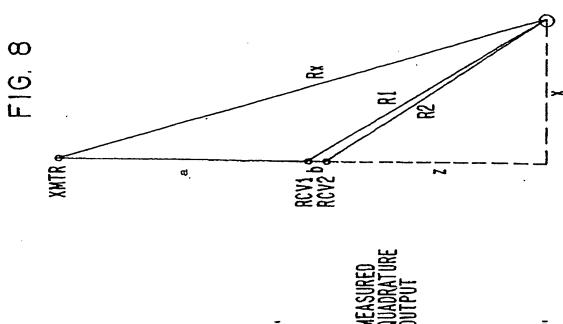


FIG. 6 0.25 0.2 0.15 1.0 0.05 0 3 5" 9 8" 10 12 13 4" 9" 10"



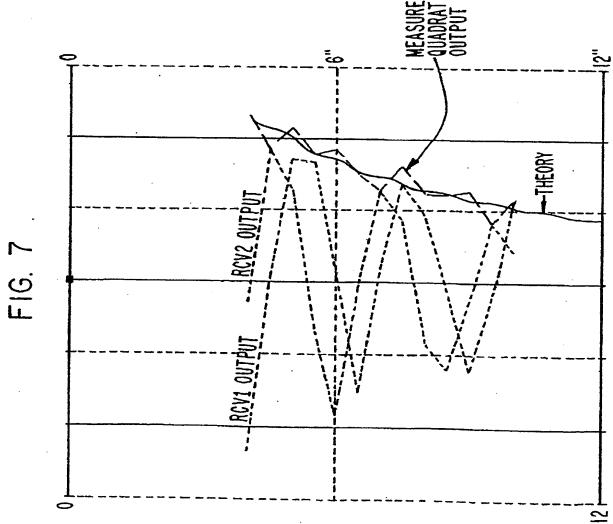


FIG. 9

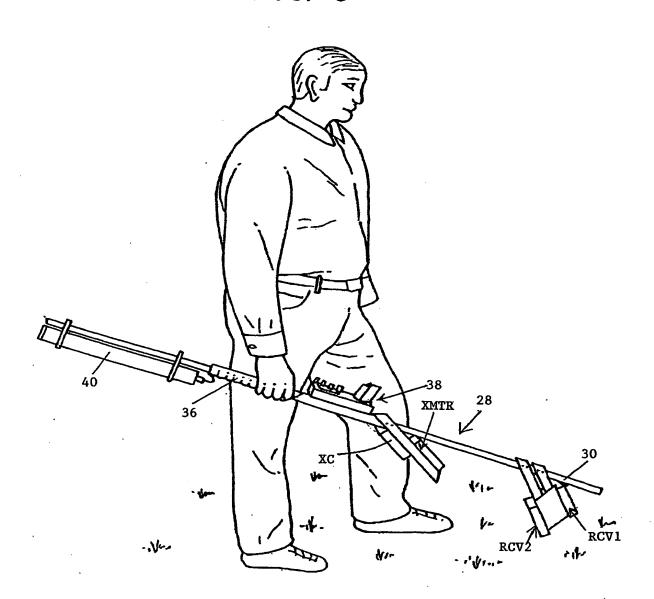


FIG. 10

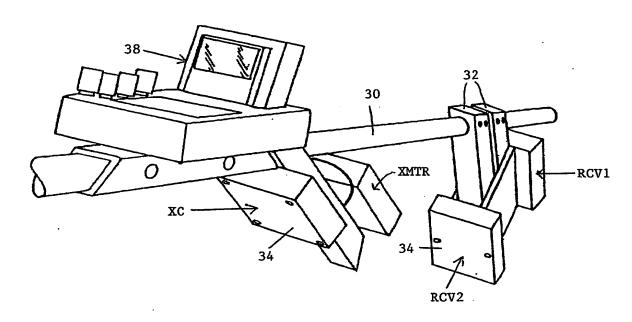
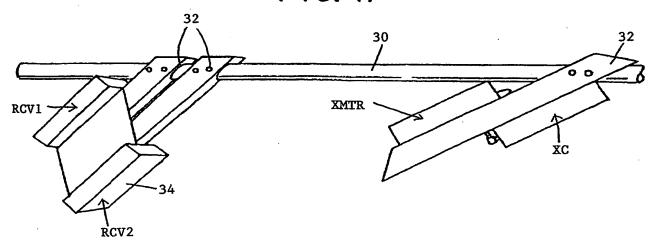


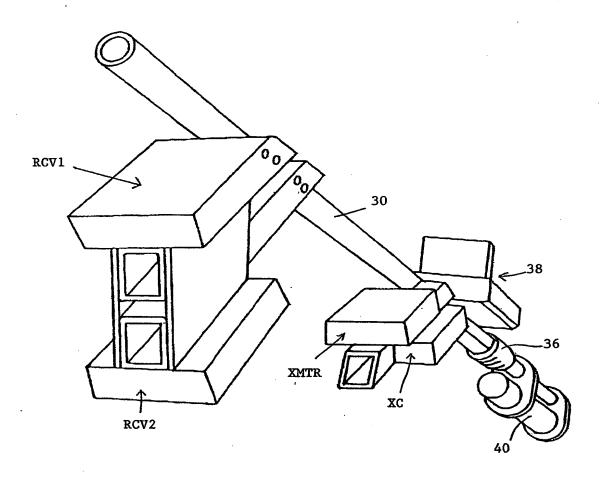
FIG. II



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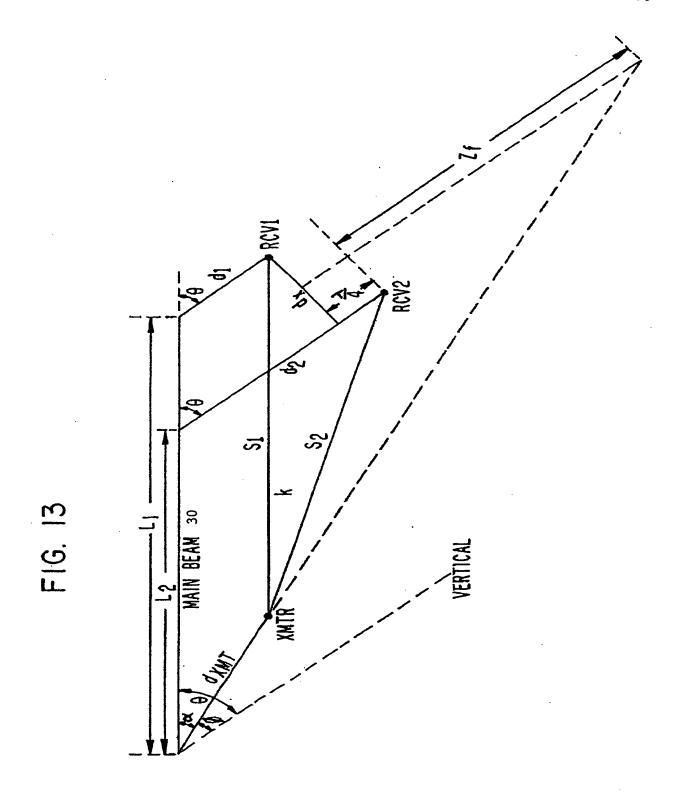
FIG. 12



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FIG. 14

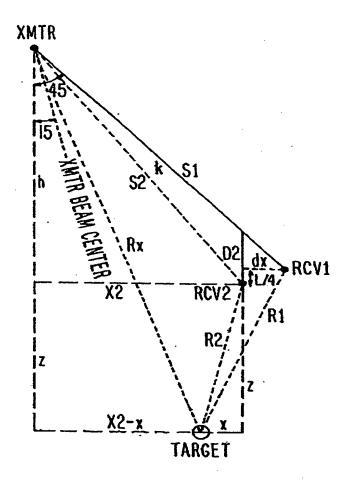


FIG. 158 OF THE SUM OF THE MULTIPLIER TAKES THE SOUARE ROOT ADS34 ANALOG TWO SQUARED INPUT SIGNALS OP27 SURMING AARP SURIS THE 2 SQUARED OC SIGNALS SQUARE IT WITH A AD633JN SQUARE IT WITH A AD633JN THE DC VOLTAGE AT THIS THE DC VOLTAGE AT THIS POINT CAN BE POSITIVE POINT CAN BE POSITIVE ANALDG MULTIPLIER OR NEGATIVE SO WE OR NEGATIVE SO WE AHALOG MULTIPLIER A GAIR ADJUST 10X TO 80X SAGA IR ADJUST 10X TO 80X A PGA205 X8 DC AMPLIFIER ATTENUATOR FOLLOWED BY ATTENUATOR FOLLOWED BY **FARIABLE GAIN 10X TO 80X** A PGA205 X8 DC AMPLIFIER VARIABLE GAIN 10X TO 80X SSMZD18T YARIABLE SSM2D18T VARIABLE FIG. 15A THIS TAKES OUT THE TRANSMITTER "BIAS" FROM IHIS TAKES OUT THE TRANSMITTER "BIAS" FROM THIS ADJUSTMENT ZERO'S THE PGA 204 OUT FHIS ADJUSTMENT ZERO'S THE PGA 204 OUT IN WITH THE LOCATOR POINTED TO THE SKY WITH THE LOCATOR POINTED TO THE SKY THE DIRECT SIGNAL THE DIRECT SIGNAL X10 DC OUT 8 AMPLIFIER AMPLIFIER XIODC PGA 204 PGA 204 CALIBRATE 💌 CALIBRATE, OUTPUT IS A LINEAR DC YOLTAGE WITH A OC VOLTAGE WITH A CONVERSION GAIN **DUTPUT IS A LINEAR** CONVERSION GAIN TRUPOWER DET TRUPOWER DET OF 7.SV/V RMS OF 7.5V/V RIKS AD8361 AD8361 RECEIVER 2 COG PERIODIC LOG PERIODIC RECEIVER) ANTERNA AHTENNA

